

IDS Form PTO/SB/08: Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/766,131
				Filing Date	01/28/2004
				First Named Inventor	Richard Perego
				Art Unit	2189
				Examiner Name	Kevin Verbrugge
Sheet	1	of	4	Attorney Docket Number	RAMB-01049US2

U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS					
Examiner Initials	Cite No.	Document Number	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code (if known)			
KV		6,477,614	11-05-2002	Leddige et al.	
		6,513,091	01-28-2003	Blackmon et al.	
		6,557,069	04-29-2003	Drehmel et al.	
		6,633,947	10-14-2003	Holman et al.	
KV		6,889,284	05-03-2005	Nizer et al. <i>Nizar et al.</i>	

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation
		Country Code Number Kind Code (if known)				

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation
KV		Request for Declaration of Interference, filed on April 13, 2006 in U.S. Patent App. No. 11/203,652 to Drehmel et al., including exhibits A through K.	
KV		Jerry D. Gibson, Editor-in-Chief, "The Mobile Communications Handbook," copyright 1996 CRC Press, Inc., p. 24	
KV		G. Doettling et al., "S/390 Parallel Enterprise Server Generation 3: A Balanced System and Cache Structure," IBM J. Res. Develop., Vol. 41 (No. 4/5), July/September 1997, pp. 405-428.	

Examiner Signature	<i>Kevin Verbrugge</i>	Date Considered	11/6-9/06
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W		European Search Report in EP 05001769				
W		EP 0 198 429	10-22-1986	NEC Corporation		
W		International Search Report in PCT/US05/17066, mailed November 2, 2005				

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KV		5,283,877	02-01-1994	Gastinel et al.	
		6,276,844	08-21-2001	Coteus et al.	
		6,369,605	04-09-2002	Bonella et al.	
		6,434,035	02-13-2002	Miersch et al.	
		6,480,409	11-12-2002	Park et al.	
		6,877,079	04-05-2005	Yoo et al.	
		6,690,191	02-10-2004	Wu et al.	
		6,853,938	02-08-2005	Jeddeloh	
		6,854,042	02-08-2005	Karabatsos	
		6,968,419	11-22-2005	Holman	
		6,970,968	11-29-2005	Holman	
		7,007,130	02-28-2006	Holman	
		7,110,400	09-19-2006	Hronik	
		2002-0112119	08-15-2002	Halbert et al.	
		2002-0124153	09-05-2002	Litaize et al.	
		2002-0144071	10-03-2002	Williams et al.	
		2003-0012229	01-16-2003	Braun	
		2003-0018880	01-23-2003	Litaize et al.	
		2003-0074490	04-17-2003	Pochmuller	
		2003-0120895	06-26-2003	Litaize et al.	
		2004-0145935	07-29-2004	Jakobs	
KV		2004-0267481	12-30-2004	Resnick	

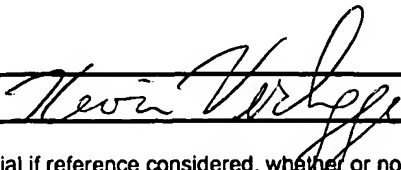
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KW		JEDEC Standard - "FBDIMM Specification: High Speed Differential PTP Link at 1.5V," JESDS-18, September 2006, available at "http://www.jedec.org/download/search/JESD8-18.pdf"	
		Excel Spreadsheet, entitled "Patents.xls," available at "http://www.jedec.org/download/search/FBDIMM/patents.xls"	
		"Patentica" letter, dated December 18, 2004, 4 pages, referenced in Excel Spreadsheet, entitled "Patents.xls."	
		"Kentron" letter, dated January 23, 2006, 1 page, referenced in Excel Spreadsheet, entitled "Patents.xls."	
		"Wintec" letter, dated March 19, 2003, 1 page, referenced in Excel Spreadsheet, entitled "Patents.xls."	
		"Wintec" letter, dated April 2, 2003, 1 page, referenced in Excel Spreadsheet, entitled "Patents.xls."	
		"AMD" letter, dated April 12, 2004, 1 page, referenced in Excel Spreadsheet, entitled "Patents.xls."	
		"Micron" letter, dated December 2, 2003, 3 pages, referenced in Excel Spreadsheet, entitled "Patents.xls."	
		"Micron" letter, dated September 16, 2005, 1 page, referenced in Excel Spreadsheet, entitled "Patents.xls."	
		"Inphi" letter, dated March 3, 2005, 1 page, referenced in Excel Spreadsheet, entitled "Patents.xls."	
		"Inphi" letter, dated June 6, 2005, 1 page, referenced in Excel Spreadsheet, entitled "Patents.xls."	
KW		"Samsung" letter, dated October 25, 2004, 1 page, referenced in Excel Spreadsheet, entitled "Patents.xls."	

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10/766131

PTO/SB/08a (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Attorney Docket Number

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W		US-5,953,215*	09-14-1999	Karabatsos	
		US-6,142,830*	11-07-2000	Looeffler	
		US-6,185,644 B1*	02-06-2001	Farmwald et al.	
		US-6,266,252 B1*	07-24-2001	Karabatsos	
		US-6,317,352 B1*	11-13-2001	Halbert et al.	
		US-6,414,904 B2*	07-02-2002	So et al.	
		US-6,446,158 B1*	09-03-2002	Karabatsos	
		US-6,493,250 B2*	12-10-2002	Halbert et al.	
		US-6,530,006 B1*	03-04-2003	Dodd et al.	
		US-6,643,752 B1*	11-04-2003	Donnelly et al.	
		US-6,449,679 B2*	09-10-2002	Ryan	
		US-6,108,731*	08-22-2000	Suzuki et al.	
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		US-5,068,650*	11-26-1991	Fernandez et al.	
		US-4,947,257*	08-07-1990	Fernandez et al.	
W		US-5,513,135*	04-30-1996	Dell et al.	
		US-6,065,092*	05-16-2000	Roy	
		US-6,185,644*	02-06-2001	Farmwald et al.	

FOREIGN PATENT DOCUMENTS

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Signature**

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Date	
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10/23 - 11/7/06

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Sheet 2

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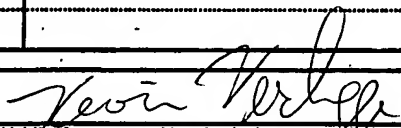
Application Number	10/766,131
Filing Date	January 28, 2004
First Named Inventor	Richard E. Perego
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	RAMB-01049US2

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RW RW		US-5,034,917*	07-23-1991	Bland et al.	
		US-4,977,498*	12-11-1990	Rastegar et al.	
		US-			
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		Art Unit	Unknown
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ICW		*Intel Developer Forum, "DDR2 Memory in 2004 Servers - Recipes For Successful Designs", September 2003	
		*QBM Alliance, Platform Conference, "Quad Band Memory (QBM tm): DDR 200/266/333 devices producing DDR400/533/667, Jan. 23-24, 2002	
		*KARABATSOS, C., "Quad Band Memory (QBM) Technology", Kentron Technologies, Inc., April 2001	
		*Quad Band Memory (QBM), "The 'QBM Enabled' VIA PT880/PM880 Chipset Solutions with the Fastest Memory",	
		*WEBER, F., "The AMD Athlon Processor: Future Directions", AMD	
		*"Intel 82804AA Memory Repeater Hub for SDRAM (MRH-S) Datasheet", Intel Corp., pp. 2-48 (Nov. 1999)	
		*"Intel 82805AA Memory Translator Hub (MTH) Datasheet", Intel Corp., pp. 1-48 (Nov. 1999)	
		*CATALDO, A., "TI Fields DSP-Based Media Processor on a DIMM", EE Times (Jan. 2000)	
		*MAC WILLIAMS, P., "PC Memory Directions For 1999 and Beyond", Intel Developer Forum, pp. 1-10 (Sep. 1998)	
PU		*JOHNSON, B., "Direct RDRAM Basic Architecture and Program Overview", Intel Developer Forum, pp. 1-14 (Sep. 1998)	

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W		*JONES, F (United Memories, Inc.), "A new era of fast dynamic RAMs", IEEE Spectrum, October 1992, pp. 43-49	
I		*FARMWALD, M. et al. (Rambus Inc.), "A fast path to one memory", IEEE Spectrum, October 1992, pp. 50-51	
I		*GJESSING, S. (University of Oslo), GUSTAVSON, D.B (Stanford Linear Accelerator Center), JAMES, D. et al. (Apple Computer Inc.), WIGGER, H. (Hewlett-Packard Co.), "A RAM link for high speed", IEEE Spectrum, October 1992, pp. 52-53	
W		*The Institute of Electrical and Electronics Engineering, Inc., "IEEE Standard for High-Bandwidth Memory Interface Based on Scalable Coherent Interface (SCI) Signaling Technology (RamLink)", 1996, pp. 1-91	

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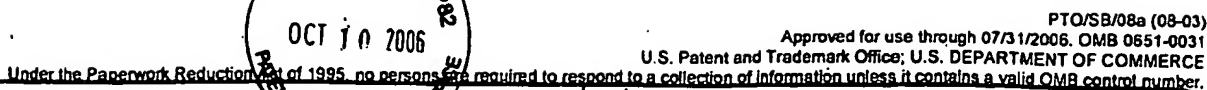
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

1

of

1

Application Number

Complete if Known

10/766.131

Filing Date

January 28, 2004

First Named Inventor

Richard E. Perego

Art Unit

2188

Examiner Name

Unknown

Attorney Docket Number

RAMB-01049US2

NON PATENT LITERATURE DOCUMENTS

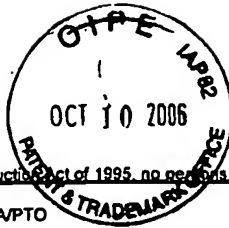
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Examiner Signature	Kevin Verhegg	Date Considered	10/23 - 11/7/06
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**INFORMATION DISCLOSURE
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Sheet 1 of 1

Complete if Known

Application Number	10/766,131
Filing Date	January 28, 2004
First Named Inventor	Perego
Art Unit	2188
Examiner Name	
Attorney Docket Number	RAMB-01049US2

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
W		US-5883839*	03-16-1999	Tosaka et al.	
		US-			
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

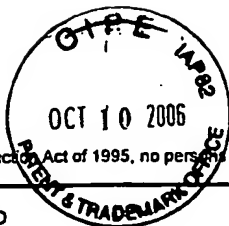
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Application Number

10/766,131

Filing Date

January 28, 2004

First Named Inventor

Perego

Art Unit

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Examiner Name

Attorney Docket Number

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
ICW		*IEEE Standard for Scalable Coherent Interface (SCI), "Microprocessor and Microcomputer Standards Subcommittee of the IEEE Computer Society", IEEE Std 1596-1992	
		*SCI, "P1596: SCI, A Scalable Coherent Interface", November 28, 1988	
		*GUSTAVSON et al., DRAFT "The Scalable Coherent Interface Project (SuperBus)", SCI, August 22, 1988, Rev. 14	
		*SCHANKE, "Proposal for Clock Distribution in SCI", SCI, May 5, 1989	
		*JAMES, "Scalable I/O Architecture for Buses", COMPCON Spring 1989, SCI, November 28, 1988	
		*VOLZ et al., "Position Paper On Global Clock For the Futurebus +", SCI, 1989	
ICW		*KRISTIANSEN et al., "Scalable Coherent Interface", to appear in Eurobus Conference Proceedings, May 1989	

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SignatureDate
Considered

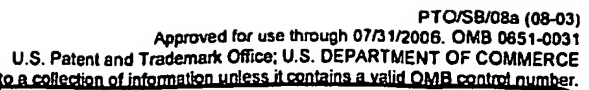
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Sheet	1	of	2
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Filing Date	January 28, 2004
First Named Inventor	Perego
Art Unit	2188
Examiner Name	
Attorney Docket Number	RAMB-01049US2

U. S. PATENT DOCUMENTS

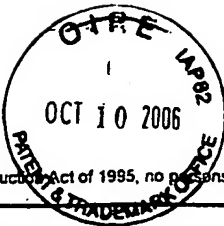
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Examiner Signature	Kevin Kasper	Date Considered	10/23 - 11/7/06
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		Application Number	10/766,131		
		Filing Date	January 28, 2004		
		First Named Inventor	Perego		
		Art Unit	2188		
		Examiner Name			
Sheet	2	of	2	Attorney Docket Number	RAMB-01049US2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
ICV		*PCT International Search Report (June 27, 2001) 4 pages	
ICV		*Microsoft Press Computer Dictionary, 3rd Ed., 1997, pp. 48 and 68	
ICV		*GUSTAVSON et al., "Macintosh HD:Desktop Folder:SLDRAMrepeaterConcerns", Sunday, November 23, 1997, 4 pages	

Examiner Signature	<i>Kevin Verheij</i>	Date Considered	10/23 - 11/7/06
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Sheet	1	of	4
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Application Number	10/766,131
Filing Date	January 28, 2004
First Named Inventor	Perego
Art Unit	2188
Examiner Name	
Attorney Docket Number	RAMB-01049US2

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Sheet 2

of 4

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Filing Date	January 28, 2004
First Named Inventor	Perego
Art Unit	2188
Examiner Name	
Attorney Docket Number	RAMB-01049US2

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
CV		US-6502161*	12-31-2002	Perego et al.	
		US-6513080*	01-28-2003	Haq	
		US-6154855*	11-28-2000	Norman	
		US-6345321*	02-05-2002	Litaize et al.	
		US-6369605*	04-09-2002	Bonella et al.	
		US-6449213*	09-10-2002	Dodd et a.	
CV		US-6625687*	09-23-2003	Halbert et al.	
		US-			
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Examiner
Signature*Kevin Vorhege*Date
Considered

10/23 - 11/7/06

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/766,131
		Filing Date	January 28, 2004
		First Named Inventor	Perego
		Art Unit	2188
		Examiner Name	
Sheet 3	of 4	Attorney Docket Number	RAMB-01049US2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
KW		*U.S. Patent Application Serial No. 10/713,868, filed 11-14-2003 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/714,025, filed 11-14-2003 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/714,026, filed 11-14-2003 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/883,474, filed 06-30-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/882,999, filed 06-30-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/859,438, filed 05-31-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/858,850, filed 05-31-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/859,060, filed 05-31-2004 by Pete D. Vogt	
		*NG, R. (Sun Microsystems Inc.), "Fast computer memories", IEEE Spectrum, October 1992, pp. 36-39	
KW		*SALTERS, R.H.W. (Philips Research Laboratories), "Fast DRAMS for sharper TV", IEEE Spectrum, October 1992, pp. 40-42	

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10/		*JONES, F (United Memories, Inc.), "A new era of fast dynamic RAMs", IEEE Spectrum, October 1992, pp. 43-49	
1/		*FARMWALD, M. et al. (Rambus Inc.), "A fast path to one memory", IEEE Spectrum, October 1992, pp. 50-51	
10/		*GJESSING, S. (University of Oslo), GUSTAVSON, D.B (Stanford Linear Accelerator Center), JAMES, D. et al. (Apple Computer Inc.), WIGGER, H. (Hewlett-Packard Co.), "A RAM link for high speed", IEEE Spectrum, October 1992, pp. 52-53	
10/		*The Institute of Electrical and Electronics Engineering, Inc., "IEEE Standard for High-Bandwidth Memory Interface Based on Scalable Coherent Interface (SCI) Signaling Technology (RamLink)", 1996, pp. 1-91	

Examiner Signature	<i>Kevin Verheij</i>	Date Considered	10/23 - 11/7/06
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KW		US-6425064 B2*	07-23-2002	Soderquist	
		US-4621339*	11-04-1986	Wagner et al.	
		US-4747070*	05-24-1988	Trottier et al.	
		US-4858107*	08-15-1989	Fedele	
		US-5602780*	02-11-1997	Diem et al.	
		US-5642444*	06-24-1997	Mostafavi	
		US-5838985*	11-17-1998	Ohki	
		US-6708248*	03-16-2004	Garrett, Jr., et al.	
		US-5982238*	11-09-1999	Soderquist	
		US-6639820 B1	10-28-2003	Khandekar et al.	
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		US-6553450 B1	04-22-2003	Dodd et al.	
		US-6510503	01-21-2003	Gillingham et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁵
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
KW		EP 0 282 070 B1*	08-03-1994	Fujitsu Limited		

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

Application Number

10/766,131

Filing Date

January 28, 2004

First Named Inventor

Perego

Art Unit

2188

Examiner Name

Attorney Docket Number

RAMB-01049US2

NON PATENT LITERATURE DOCUMENTS

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Initials*Cite
No. 1

Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

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KU

**JEDEC Standard No. 21-C, Pages 4.20.4-1 through 4.20.4-69, 4.20.4- 184 PIN, PC1600/2100 DDR SDRAM
REGISTERED DIMM DESIGN SPECIFICATIONM, Revision 1.2**

1

MICRON, 240-Pin 256MB, 512MB, 1GB DDR2 SDRAM FBD/IMM (SR, FB x72) Features, Micron Technology, Inc., 2004, pp. 1-36

**Examiner
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Kevin Verheij

Date Considered

10/23-11/7/08

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STATEMENT BY APPLICANT**

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Sheet 1

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Application Number	10/766,131
Filing Date	January 28, 2004
First Named Inventor	Perego
Art Unit	2188
Examiner Name	
Attorney Docket Number	RAMB-01049US2

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
KW		US-5926839*	07/20/1999	Katayama	
		US-6272609 B1*	08/07/2001	Jeddeloh	
		US-6292877 B1*	09/18/2001	Ryan	
		US-6449703 B2*	09/10/2002	Jeddeloh	
		US-6480927 B2*	11/12/2002	Bauman	
		US-6604180 B2*	08/05/2003	Jeddeloh	
		US-6622224 B1*	09/16/2003	Cloud	
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		US-2002/0184462A1*	12/05/2002	Jeddeloh	
		US-6477592 B1*	11/05/2002	Chen et al.	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

1

of

1

Application Number

Complete if Known

10/766,131

Filing Date

January 28, 2004

First Named Inventor

Perego

Art Unit

2188

Examiner Name

Attorney Docket Number

RAMB-01049US2

NON PATENT LITERATURE DOCUMENTS

[illegible]

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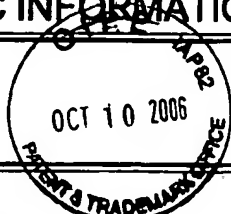
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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0



Title of
Invention

CONFIGURABLE WIDTH BUFFERED MODULE

Application Number: 10/766131 *10/766131*
Confirmation Number: 3149
First Named Applicant: Richard Perego
Attorney Docket Number: RAMB-01049US2
Art Unit: 2188
Search string: (5513377 or 6526469 or 6185654 or 6530033 or 5228132).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

Init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<input checked="" type="checkbox"/>	1	5513377 *	1996-04-30	Capowski et al.			
<input type="checkbox"/>	2	6526469 *	2003-02-25	Drehmel et al.			
<input type="checkbox"/>	3	6185654 *	2001-02-06	Van Doren			
<input type="checkbox"/>	4	6530033 *	2003-03-04	Raynham et al.			
<input checked="" type="checkbox"/>	5	5228132 *	1993-07-13	Neal et al.			

Signature

Examiner Name	Date
<i>Kevin Verheij</i>	10/23 - 11/7/06

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

CONFIGURABLE WIDTH BUFFERED MODULE

Application Number: 10/766131

10/766131

Confirmation Number: 3149

First Named Applicant: Richard Perego

Attorney Docket Number: RAMB-01049US2

Art Unit: 2188

Search string: (6330687 or 5371880 or 5475818 or 5400360 or 4631666 or 5655113 or 5511224 or 5574945 or 5917760 or 6016282 or 6449727 or 4644532 or 4667305 or 4747100 or 4864563 or 5228134 or 5355467 or 5408646 or 5537394 or 5544342 or 5553266 or 5557266 or 5604735 or 5630095 or 5659710 or 5787083 or 5802054 or 5802565 or 5805798 or 5848145 or 5884036 or 5893921 or 5900017 or 5901294 or 5911052 or 5913044 or 5923893 or 6247100 or 20040095838 or 20040260991 or 20040250181 or 20040250153 or 20040150024 or 20040246786 or 20040246785 or 20040246767 or 20040015650 or 20030177313 or 20020083287).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<input checked="" type="checkbox"/>	1	6330687 *	2001-12-11	Griffith			
<input checked="" type="checkbox"/>	2	5371880 *	1994-12-06	Bhattacharya			
<input checked="" type="checkbox"/>	3	5475818 *	1995-12-12	Molyneaux et al.			
<input checked="" type="checkbox"/>	4	5400360 *	1995-03-21	Richards et al.			
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<input checked="" type="checkbox"/>	6	5655113 *	1997-08-05	Leung et al.			
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23	5604735 *	1997-02-18	Levinson et al.
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30	5848145 *	1998-12-08	Gallagher et al.
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32	5893921 *	1999-04-13	Bucher et al.
33	5900017 *	1999-05-04	Genduso et al.
34	5901294 *	1999-05-04	Tran et al.
35	5911052 *	1999-06-08	Singhal et al.
36	5913044 *	1999-06-15	Tran et al.
37	5923893 *	1999-07-13	Moyer et al.
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US Published Applications

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Init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20040095838	2004-05-20	Li			
	2	20040260991	2004-12-23	Vogt et al.			
	3	20040250181	2004-12-09	Vogt et al.			

<input checked="" type="checkbox"/>	4	20040250153 *	2004-12-09	Vogt
<input checked="" type="checkbox"/>	5	20040150024 *	2004-12-09	Vogt
<input checked="" type="checkbox"/>	6	20040246786 *	2004-12-09	Vogt
<input checked="" type="checkbox"/>	7	20040246785 *	2004-12-09	Vogt
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<input checked="" type="checkbox"/>	9	20040015650 *	2004-01-22	Zumkehr et al.
<input checked="" type="checkbox"/>	10	20030177313 *	2003-09-18	Iyer et al.
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Remarks

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Examiner Name	Date
<i>Kevin Vorlage</i>	10/23-11/7/06

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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of

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Application Number	10/766,131
Filing Date	January 28, 2004
First Named Inventor	Richard E. Perego
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	RAMB-01049US2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
1W		*GILLINGHAM, P., "SLDRAM Architectural and Functional Overview", SLD RAM Consortium, SLD RAM Inc., pp. 1-14 (Aug. 1997)	
		**Draft Standard for a High-Speed Memory Interface (Synclink)", Draft 0.99 IEEE P1596.7-199X, IEEE Inc., pp. 1-51 (1996)	
		*WIGGERS, H., "SyncLink A Proposal for an Implementation of IEEE P1596.4", Hewlett Packard, pp. 1-20 (Mar. 1995)	
		**KMM377S1620CT2 SDRAM Module Datasheet", Samsung Electronics Inc., pp. 1-2 (Nov. 1998)	
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		**SLDRAM 400Mb/s/pin Command/Address Repeater", SLD10400 Rev .5, SLD RAM Consortium, pp. 1-12 (Jan. 7, 1998)	
		*Minutes of meeting no. 75 JEDEC JC-42.3 Committee on RAM Memories, and referenced attachment "Y" of the presentation titled "Synclink", May 24, 1995, New Orleans	
		*Intel Developer Forum, "Fully Buffered DIMM (FB-DIMM) Server Memory Architecture: Capacity, Performance, Reliability, and Longevity", February 18, 2004	
1W		*Intel Developer Forum, "Fully Buffered DIMM (FB-DIMM) Design Considerations", February 18, 2004	

Examiner
Signature

Kevin Verheij

Date
Considered

10/23 - 11/7/06

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